

LSI Docket No. 03-0827

Specification Amendment

Please amend the Specification as follows:

At page 12, please re-write the second full paragraph as follows:

--Processor 206, memory 207 and N-chip 208 form N-way functionality 205 for storage controller 102. In one embodiment, N-way functionality 205 and stand-alone functionality of host interfaces 204-1 and 204-2 is configured as a PIC that interfaces to storage controller 102 indicated by dotted line 210. For example, storage controller 102 may be configured to connect to a PIC having either of or both of N-way functionality 205 and stand-alone functionality. Such an embodiment may accommodate flexible reconfiguration of storage controller 102 and improved storage performance features. In a solely stand-alone embodiment, host interfaces 204-1 and 204-2 may connect directly to processor 201 via a bus connection bypassing the N-chip 208 connectivity. In other embodiments, N-way functionality 205 may be populated on the PIC but host interfacing 204-1 and ~~[[20402]] 204-02~~ may be removed. In an embodiment where both N-way functionality 205 and host connectivity through interfaces 204-1 and 204-2 are included, an appropriately populated PIC may be used.--